

Amendments to the Claims

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims

1-10. (Canceled)

11. (Currently Amended) A signal line driver circuit comprising:

a shift register;

a latch circuit, electrically connected to the shift register, comprising a plurality of pairs of current source circuits, wherein each of the plurality of pairs of current source circuits is ~~configured to receive a set signal and a signal current, and to control an output current value depending on a value of the signal current~~ includes a transistor having a gate, a source and a drain; and

a changing over circuit electrically connected to the plurality of pairs of current source circuits and a plurality of signal lines,

wherein each of the plurality of pairs of current source circuits is configured to control an output current value depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a signal current to the transistor while the gate and the drain of the transistor are electrically connected to each other,

wherein the changing over circuit is configured to select one pair of current source circuits from the plurality of pairs of current source circuits for electrically connecting to each of the plurality of signal lines, and

wherein the shift register is configured to output the set signal.

12. (Previously Presented) A signal line driver circuit comprising:

a shift register;

a latch circuit, electrically connected to the shift register, comprising:

a plurality of pairs of current source circuits, wherein each of the plurality of pairs of current source circuits ~~is configured to receive a set signal and a signal current, and to control an output current value depending on a value of the signal current~~ includes a transistor having a gate, a source and a drain;

a first switch provided between the shift register and each of the plurality of pairs of current source circuits; and

a second switch, and

a changing over circuit electrically connected between the plurality of pairs of current source circuits and a plurality of signal lines,

wherein each of the plurality of pairs of current source circuits is configured to control an output current value depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a signal current to the transistor while the gate and the drain electrically are connected to each other,

wherein the changing over circuit is electrically connected to a particular pair of current source circuits through the second switch,

wherein the changing over circuit is configured to select one pair of current source circuits from the plurality of pairs of current source circuits for electrically connecting to each of the plurality of signal lines,

wherein the shift register is configured to output the set signal, and

wherein the first and second switches are configured to be controlled by a latch pulse.

13-80. (Canceled)

81. (Previously Presented) A signal line driver circuit comprising:

a plurality of current source circuits, wherein each of the plurality of current source circuits includes a transistor having a gate, a source and a drain, wherein each of the plurality of current source circuits is configured to be supplied with a first current and to supply a second current, and wherein a value of the second current depends on ~~a value of the first current~~ a voltage between the gate and the source of the transistor of the current source circuit that is

generated by supplying the first current to the transistor while the gate and the drain are electrically connected to each other;

a plurality of signal lines; and

a selector circuit electrically connected between the plurality of current source circuits and the plurality of signal lines, wherein the selector circuit is configured to select one of the plurality of signal lines to which the second current is supplied.

82-91. (Canceled)

92. (New) The signal line driver circuit according to claim 11, further comprising a capacitor wherein one electrode of the capacitor is electrically connected to the source of the transistor and the other electrode of the capacitor is electrically connected to the gate of the transistor.

93. (New) The signal line driver circuit according to claim 12, further comprising a capacitor wherein one electrode of the capacitor is electrically connected to the source of the transistor and the other electrode of the capacitor is electrically connected to the gate of the transistor.

94. (New) The signal line driver circuit according to claim 81, further comprising a capacitor wherein one electrode of the capacitor is electrically connected to the source of the transistor and the other electrode of the capacitor is electrically connected to the gate of the transistor.